

EGA41206V12A0 Engineering Specification

1. Scope

This specification is applied to electrostatic discharge (ESD) protection. It is designed to protect the high-speed data lines against ESD transients. It has very low capacitance and fast turn on times makes it ideal for data and transmission lines with high data rates. According to the special property of device, we recommend not to use on such application as: DC/AC power line, keypad, and button circuit.

For RoHS Compliance.

Feature

- Protection against ESD voltages and currents (IEC61000-4-2 Level 4)
- Extremely quick response time (<1ns) present ideal ESD protection
- Extremely low capacitance (0.25pF typical)
- Extremely low leakage current
- Bi-directional device
- SMD (Surface Mount Device)
- Zero signal distortion

Applications

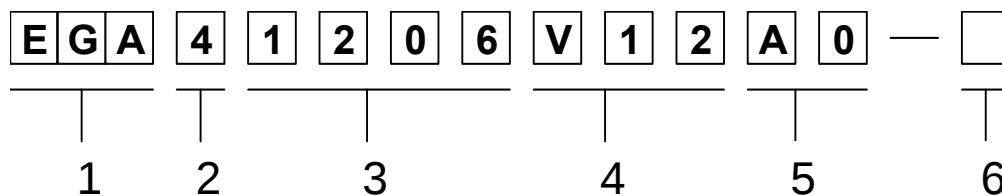
EGA41206V12A0 is applied to high speed signal interface.

- Antenna circuit
- USB2.0/3.0
- IEEE-1394
- DVI
- HDMI

Product Model

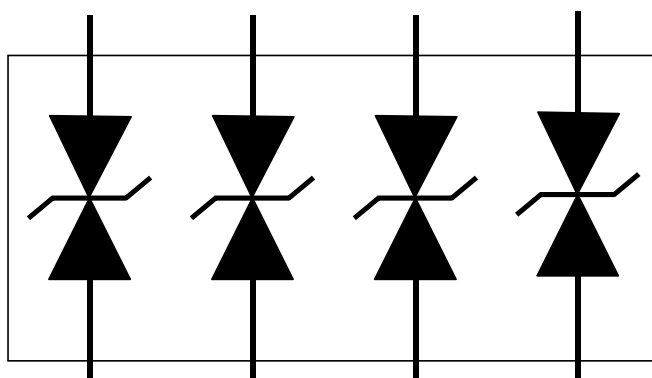
- Digital Video Equipment
- Mobile Phone
- GPS Antenna
- Bluetooth Communication Equipment

2. Explanation of Part Number



- 1 : ESDGUARD Series
 2 : Elements
 3 : Chip size, EIA 1206
 4 : Max rated voltage, VDC
 5 : "A0" : Model Code
 6 : Suffix for Special Code

3. Circuit symbol



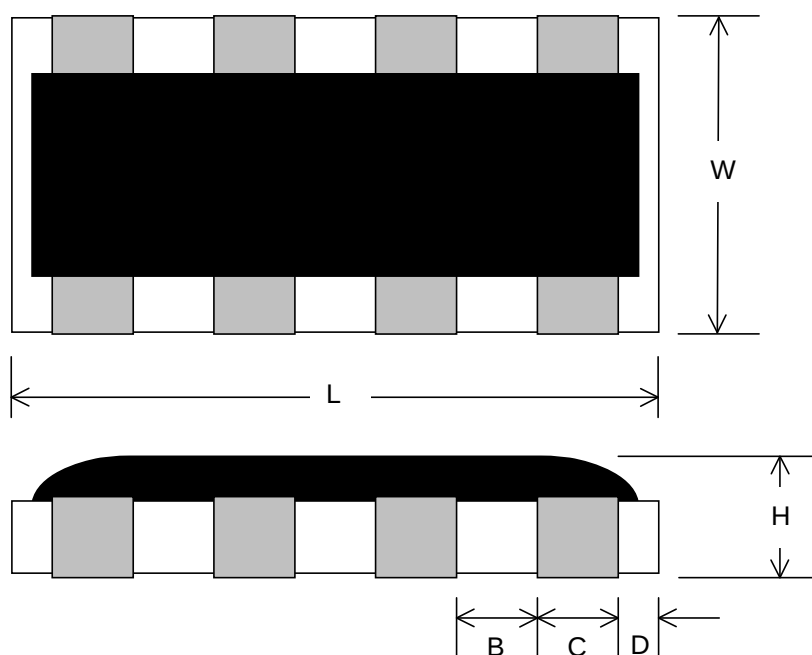
4. Construction & Dimensions

4.1. Substrate : Ceramic (Alumina)

4.2. Encapsulate : Polymer

4.3. End termination : Ag/Ni/Sn

4.4. Construction & Dimension :



Unit: mm

L	W	H	B	C	D
3.2±0.1	1.6±0.1	0.50±0.1	0.4±0.2	0.4±0.2	0.2±0.1

5. General specifications

5.1. Temperature Specifications

Operating Temperature range	: -40°C to +85°C
Storage Temperature range	: -55°C to +125°C

5.2. Environmental Specifications

Item	Specifications	Test condition	Reference
Bias humidity	$I_L \leq 10 \mu A$	90%RH, 40°C, rated voltage 12VDC, 1000 hrs	MIL-STD-202 Method 103
Thermal Shock	$I_L \leq 10 \mu A$	-40°C to 85°C, 30 min. cycle, 5 cycles	JIS C 0025 (1998) Test Na
High Temperature load voltage	$I_L \leq 10 \mu A$	Rated voltage, 85°C, 1000 hrs	MIL-STD-202G Method 108
Solder leach resistance	$I_L \leq 10 \mu A$	260°C, 10s	MIL-STD-202G Method 210F
Vibration	$I_L \leq 10 \mu A$	The entire frequency range : 10~55Hz 1.5mm amplitude 2 hours for each of X, Y, Z directions	MIL-STD-202G Method 201A

I_L – Leakage current at rated voltage, the maximum leakage current was measured after reliability test.

6. Performance Characteristics

	Symbol	Unit	Min.	Typical	Max.
Rated voltage	VDC	V			12
Leakage current	I_L	μA		0.01	
Trigger voltage	Vt	V		300	
Clamping voltage	Vc	V		30	
Capacitance, @1MHz	Cp	pF		0.25	0.5
Response time		ns			1
ESD voltage capability, Contact discharge mode		kV		8	
ESD voltage capability, Air discharge mode		kV		15	
ESD pulse withstand		pulses		1000	

Vt – measurement by using Transmission Line Pulse (TLP)

Vc –measurement by using Transmission Line Pulse (TLP)

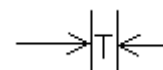
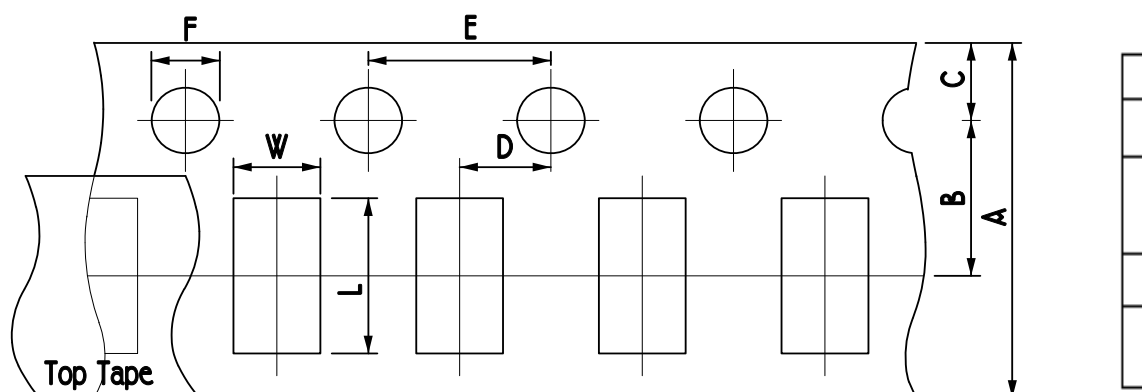
Cp – Device capacitance measured with 1Vrms

7. Taping Package and Label Marking

7.1. Packaging method

- 7.1.1. Products shall be heat-sealed in the chip pocket, spacing pitch 2-mm of paper carrier tape with cover tape, and the carrier tape shall be reeled to the reel.
- 7.1.2. Tape material to be paper. Tape thickness to be 0.75 ± 0.05 mm.
- 7.1.3. Cover tape adhesion to be 40 ± 15 grams.

7.2. Carrier tape dimensions

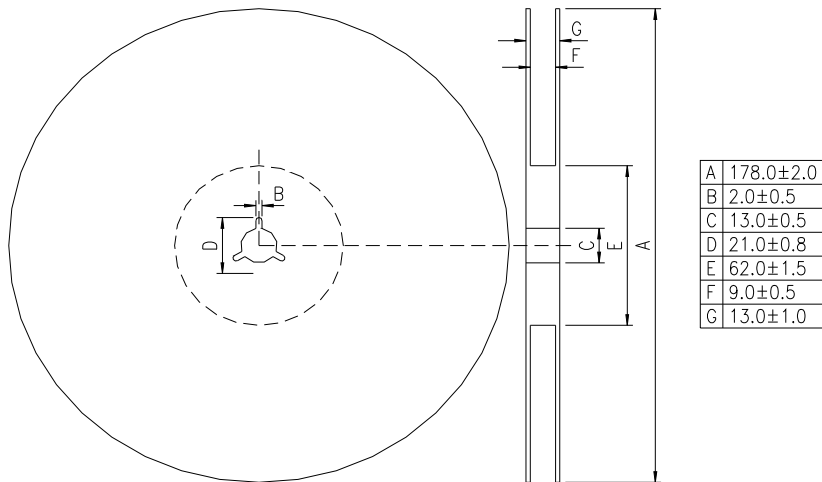


Unit: mm

	1206
A	8.00 ± 0.30
B	3.50 ± 0.05
C	1.75 ± 0.10
D	2.00 ± 0.05
E	4.00 ± 0.10
F	1.50 ± 0.10
L	3.62 ± 0.2
W	2.02 ± 0.2
T	0.75 ± 0.05

7.3. Taping reel dimensions

Unit: mm



7.4. Taping specifications

There shall be the portion having no product in both the head and the end of taping, and there shall be the cover tape in the head of taping.

7.5. Label Marking

The label specified as follows shall be put on the side of reel.

- (1) Part No.
- (2) Quantity
- (3) Lot No.

* Part No. And Quantity shall be marked on outer packaging.

7.6. Quantity of products in the taping package

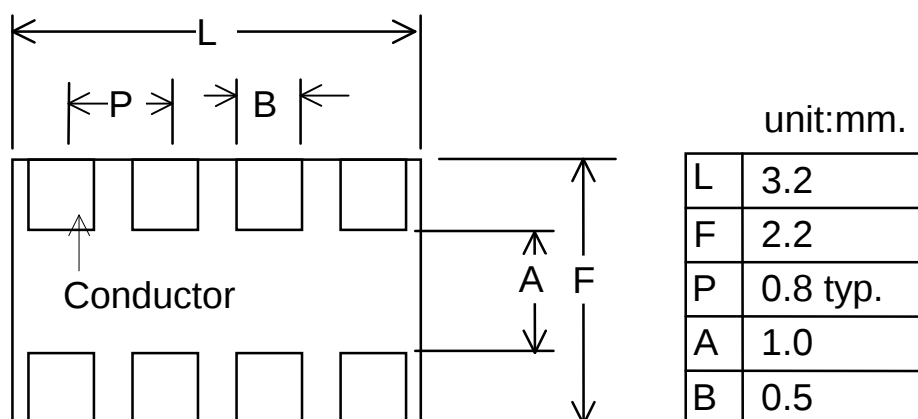
- (1) Standard quantity : 5000pcs/Reel
- (2) Shipping quantity is a multiple of standard quantity.

8. Precautions for Handling

8.1. Solder cream in reflow soldering

Refer to the recommendable land pattern as printing mask pattern for solder cream.

- (1) Print solder in a thickness of 0.15 to 0.2 mm.



8.2. Precaution for handling of substrate

Do not exceed to bend the board after soldering this product extremely. (reference examples)

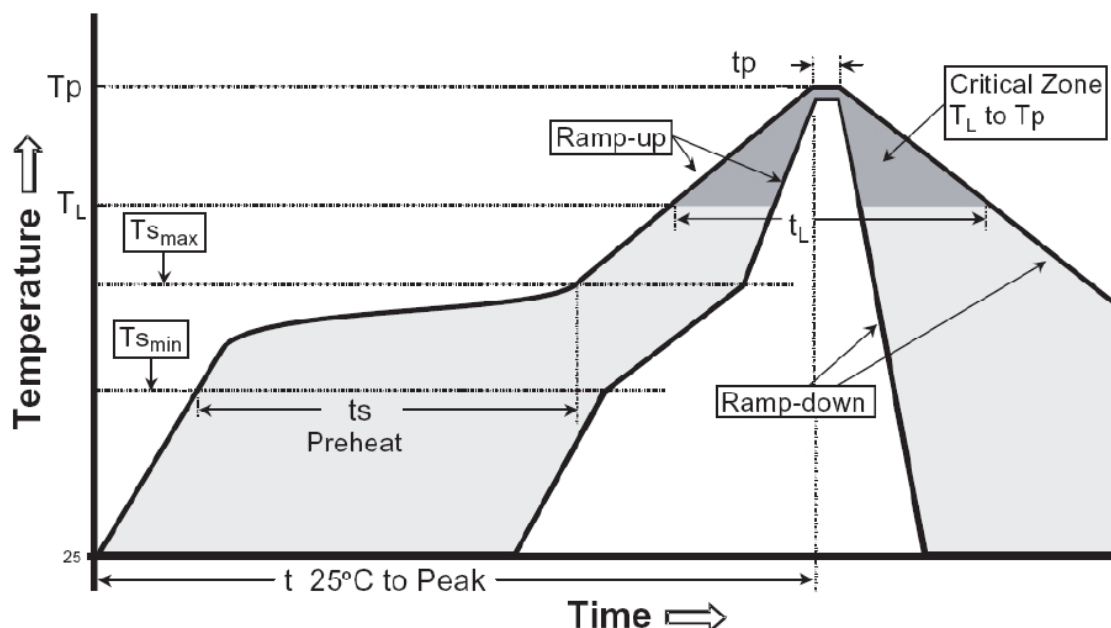
- Mounting place must be as far as possible from the position, which is close to the break line of board, or on the line of large holes of board.
- Do not bend extremely the board, in mounting another components.
If necessary, use back-up pin (support pin) to prevent from bending extremely.
- Do not break the board by hand. We recommend to use the machine or the jig to break it.

8.3. Precaution for soldering

Note that this product will be easily damaged by rapid heating, rapid cooling or local heating.

Do not give heat shock over 100°C in the process of soldering. We recommend to take preheating and gradual cooling.

8.4. Recommendable reflow soldering



Reference IPC-020c-5-1

Profile Feature	Pb free Assembly
Average Ramp Rate (Ts max to Tp)	3 °C/second max
Preheat - Temperature Min (Ts_{min}) - Temperature Min (Ts_{max}) - Time(ts_{min} to ts_{min})	150°C 200°C 60-180 seconds
Time maintained above: - Temperature (T_L) - Time (t_L)	217°C 60-150 seconds
Peak Temperature (T _p)	260°C +0/-5 °C
Time within 5 °C of actual Peak Temperature (T _p)	20-40 seconds
Ramp-Down Rate	6 °C/second max.
Time 25°C to Peak Temperature	8 minutes max

8.5. Soldering gun procedure

Note the follows, in case of using solder gun for replacement.

- (1) The tip temperature must be less than 280°C for the period within 3 seconds by using soldering gun under 30 W.
- (2) The soldering gun tip shall not touch this product directly.

8.6. Soldering volume

Note that excess of soldering volume will easily get crack the body of this product.

8.7. Taping Package Storage Condition

Storage Temperature : 5 to 40 °C

Relative Humidity: < 65%RH

Storage Time : 12 months max